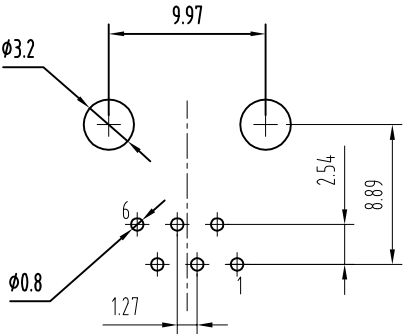
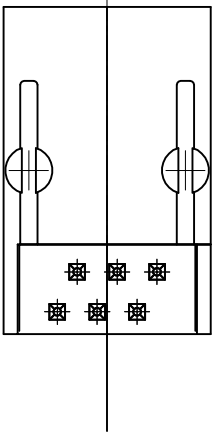
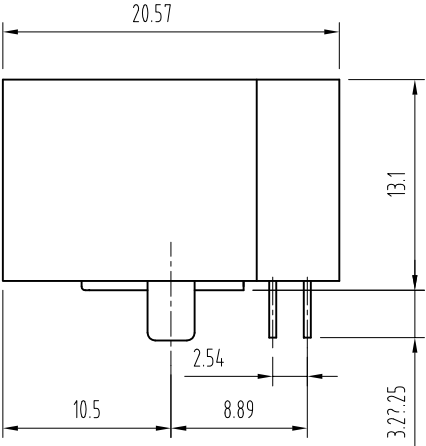
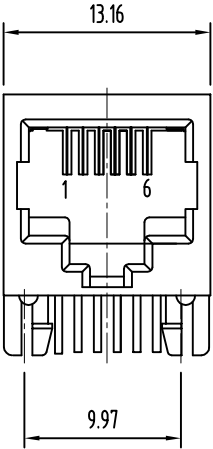


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout
Component Side Shown

NOTES:

MATERIAL :

- 1.HOUSING MATERIAL :GLASS FILLED POLYESTER UL94V-0.
2.CONTACT MATERIAL :PHOSPHOR BRONZE $\phi 0.45\text{mm}$.
3.PLATING :GOLD PLATING OVER NICKEL.

ELECTRICAL:

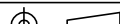
- 1.VOLTAGE RATING :125 VAC RMS.
2.CURRENT RATING :1.5 AMP.
3.CONTACT RESISTANCE :30 MILLIOHMS MAX.
4.INSULATION RESISTANCE :500 MEGOHMS MIN @ 500V DC .
5.DIELECTRIC WITHSTANDING RESISTANCE :1000V AC RMS 50Hz. 1MIN.

MECHANICAL:

- 1.DURRABILITY :750 CYCLES MIN.
2.PCB RETENTION PRE-SOLDER :1 LB MIN.

ENVIRONMENTAL:

1. STORAGE : -40°C~+85°C
2. OPERATION : 0° TO 70°.

DETACHED LISTS	MM (INCH)		DFTO:	DATE:	Coorle Technology				
	TOLERANCES EXCEPT AS NOTED		CHKD:	DATE:					
			MFG:	DATE:					
			APPVL:	DATE:					
	MM .0 ± 0.15 .00 ± 0.075 .000 ± 0.05		± ± ±	MATERIAL :		TITLE 59A JACK W/O SHIELD			
	ANGLES ± 0.5		Q.T'Y :						
			FINISH :						DRAWING NO. TES9A1001
	THIRD ANGLE PROJECTION		SCALE :	/PART NO.		A3	0		
								DO NOT SCALE DRAWING	
								SHEET X OF Y	